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Substitute for Form 1449A/PTO (Modified) (use as many sheets as necessary)		Attorney Docket No.: 2489.P002DC	Application Number: Not yet assigned
		First Named Inventor: Varadarajan Srinivasan	10/743, 962
		Filing Date: Herewith 12/22/03	

U.S. PATENT DOCUMENTS

Exam. Initial*	Cite No. ¹	U.S. Patent Document		Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number Code ²	Kind (If known)			
BSP		3,257,646		Roth	6/21/66	
BSP		3,353,159		Lee, III	11/14/67	
BSP		3,602,899		Lindquist et al.	8/31/71	
BSP		3,675,211		Raviv	7/4/72	
BSP		3,685,020		Meade	8/15/72	
BSP		3,868,642		Sachs	2/25/75	
BSP		4,112,502		Scheuneman	9/5/78	
BSP		4,244,033		Hattori	1/6/81	
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		Office ³	Number ⁴	Kind Code ⁵ (If known)				
BSP		Japan	08167295			6/25/96		
BSP		EPO	0660332A			12/23/93		

Examiner Signature		Date Considered 9/17/04	
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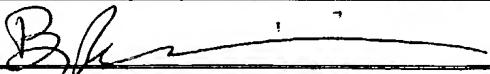
Substitute for Form 1449A/PTO (Modified) (use as many sheets as necessary)		Attorney Docket No.: 2489.P002DC	Application Number: Not yet assigned
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BJP		4,646,271		Uchiyama et al.	2/24/87	
BJP		4,656,626		Yudichak et al.	4/7/87	
BJP		4,670,858		Almy	6/2/87	
BJP		4,747,080		Yamada	5/24/88	
BJP		4,758,982		Price	7/19/88	
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BJP		4,785,398		Joyce et al.	11/15/88	
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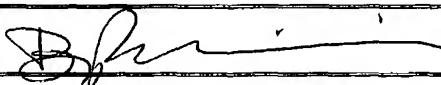
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BJP		4,897,813		Kumbasar	1/90	
BJP		4,903,234		Sakuraba et al.	2/20/90	
BJP		4,928,260		Chuang et al.	5/22/90	
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BJP		5,226,005		Lee et al.	7/6/93	
BJP		5,265,100		McClure et al.	11/23/93	
BJP		5,267,213		Sung et al.	11/93	
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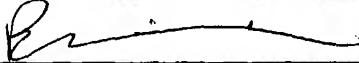
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BJP		5,414,704		Spinney	5/9/95	
BJP		5,428,565		Shaw	6/95	
BJP		5,440,709		Edgar	8/8/95	
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BJP		5,455,576		Clark, II et al.	10/3/95	
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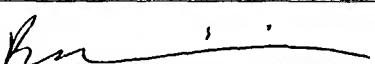
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BJP		5,485,418		Hiraki et al.	1/16/96	
BJP		5,490,102		Jubran	2/6/96	
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BJP		5,513,134		Cooperman et al.	4/30/96	
BJP		5,517,441		Dietz et al.	5/14/96	
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BJP		5,706,224		Srinivasan et al.	1/6/98	
BJP		5,841,874		Kempke et al.	11/98	
BJP		5,860,085		Stormon et al.	1/99	
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BJP		6,148,364		Srinivasan et al.	11/14/00	
BJP		6,219,748		Srinivasan et al.	4/17/01	
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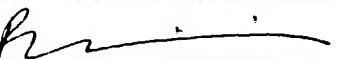
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BJP		5,905,668		Takahashi	5/18/99	
BJP		5,893,931		Peng	4/13/99	
BJP		5,818,873		Wall	10/6/98	
BJP		5,758,148		Lipovski	5/26/98	
BJP		5,649,149		Stormon	7/15/97	
BJP		5,621,677		Jones	4/15/97	
BJP		5,485,526		Tobin	1/16/96	
BJP		5,339,268		Machida	8/16/94	
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OTHER ART - NO PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published	Translation ²
BSP		PCT International Search Report - PCT/US 98/22000 - International Filing Date October 15, 1998.	
BSP		Masao Akata, "A Scheduling Content-Addressable Memory for ATM Space-Division Switch Control", IEEE International Solid State Circuits Conference, Feb. 1991, New York.	
BSP		Ghose et al., "Response Pipelined CAM Chips: The First Generation and Beyond", 7 th International conference on VLSI Design, Ja. 1994, pp. 365-368.	
BSP		Music Semiconductors, "MU9C1480 LANCAM" Advance Information, March 22, 1990, pp. 1-11.	
BSP		Music Semiconductors, "MU9C1480 LANCAM™" Preliminary Specification, January 15, 1991. Rev O, pp. 1 page total.	
BSP		KLSI, "Address Processor" KE5B064H series -Dual Port and Fixed Table Type-, Version 1.0.1, published approximately late 1995 or early 1996, pp. 1-1 through 14-1.	
BSP		KLSI, "KE5B064A1 Address Processor" Version 2.0.2, published approximately late 1995 or early 1996, pp. 1-1 through 12-1.	

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BSP		Landrock et al., "Associative Storage Module", IBM Technical Disclosure Bulletin, Vol. 25, No. 5, October 1982, pp. 2341-2342.	
BSP		Motorola Semiconductor Technical Data, "Advance Information 16K x 64 CAM" MCM69C432, January 1996, pp. 4 pages total.	
BSP		GEC Plessey Semiconductors, "P2800 Multi-port Content Addressable Memory Functional Product Specification GPS-FPS-2800-12", January 1996, pp. 1-102.	
BSP		Advanced Micro Devices, "Final Am99C10A 256 x 48 Content addressable Memory" Publication No. 08125, Rev. G, December 1992, pp. 1-21.	
BSP		Music Semiconductors, "MU9C2480 LANCAM Preliminary Data Sheet", August 25, 1995, pp. 1-24.	
BSP		GEC Plessey Semiconductors Preliminary Information, P2800 2K x 64 BIT MULTI-PORT CONTENT ADDRESSABLE MEMORY", February 1997, pp. 1-15.	
BSP		Music Semiconductor, "The MU9C1480 LANCAM Handbook, Rev. 3, November 1994, pp. 1-1 through 7-12.	
BSP		Soo-Ik Chae, et al., "Content-Addressable Memory for VLSI Pattern Inspection", IEEE Journal of Solid-State Circuits, Vol. 23, No. 1, February 1988, pp. 74-78.	
BSP		Yong-Chul Shin, et al., "A Special-Purpose Content Addressable Memory Chip for Real-Time Image Processing", IEEE Journal of Solid-State Circuits, Vol. 27, No. 5, May 1992, pp. 737-744.	
BSP		Sergio R. Ramirez-Chavez, "Encoding Don't Cares in Static and Dynamic Content-Addressable Memories", Transaction Briefs" IEEE Transactions on Circuits and System-II: Analog and Digital Signal Processing, Vol. 39, No. 8, August 1992, pp. 575-578.	

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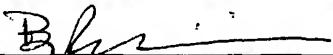
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Substitute for Form 1449A/PTO (Modified) (use as many sheets as necessary)	Attorney Docket No.: 2489.P002DC	Application Number: <u>Not yet assigned</u>
	First Named Inventor: Varadarajan Srinivasan	
	Filing Date: <u>Herewith</u> 12/22/03	

OTHER ART - NO PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published	Translation ²
BJP		Ian N. Robinson, Hewlett-Packard Laboratories, "Pattern-Addressable Memory", June 1992, pp. 20-30.	
BJP		Keikichi Tamaru, "The Trend of Functional Memory Development", INVITED PAPER Special Issue on LSI Memories, IEICE Trans. Electron., Vol. E76-C, No. 11, Nov. 1993, pp. 1545-1554.	
BJP		Takeshi Ogura, et al., "A 4-kbit Associative Memory LSI", IEEE Journal of Solid-State Circuits, Vol. SC-20, No. 6, December 1985, pp. 1277-1281.	
BJP		Hiroshi Kadota, et al., "An 8-kbit Content-Addressable and Reentrant Memory", IEEE Journal of Solid-State Circuits, Vol. SC-20, No. 5, October 1985, pp. 951-956.	
BJP		Simon R. Jones, et al., "A 9-kbit Associative Memory for High-Speed Parallel Processing Applications", IEEE Journal of Solid-State Circuits, Vol. 23, No. 2, April 1988, pp. 543-548.	
BJP		Anthony J. McAuley, et al., "A Self-Testing Reconfigurable CAM", IEEE Journal of Solid-State Circuits, Vol. 26, No. 3, March 1991, pp. 257-261.	

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